Low Resistance Metal Alloy Power Resistors



LRMAP1216

Features:

- 4-terminal Kelvin J-lead terminations
- Resistance range $0.5m\Omega$ and $1m\Omega$
- 3 and 5W rating in compact footprint
- Robust welded construction
- Low inductance
- AEC-Q200 Qualified



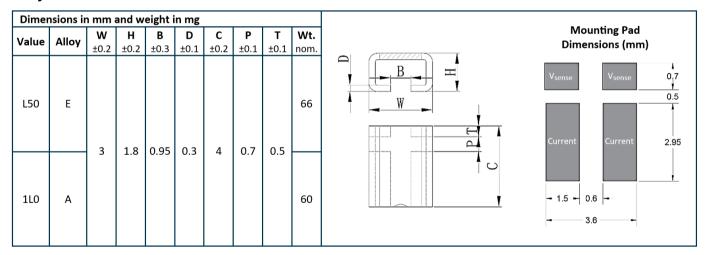


All parts are Pb-free and comply with EU Directive 2011/65/EU amended by (EU) 2015/863 (RoHS3)

Electrical Data

		LRMAP1216			
Resistance value	mΩ	0.5	1.0		
Power rating, P _{r130}	W	5	3		
Alloy		E	Α		
TCR (resistive alloy)	ppm/°C	±10	±15		
TCR (resistor)	ppm/°C	±50			
Resistance tolerance	%	±0.5, ±1, ±3, ±5			
Inductance	nH	<3			
Ambient temperature range	°C	-65 to +170			

Physical Data



Marking

The component is marked with ohmic value (using R to indicate decimal position in ohms).

Solvent Resistance

The component is resistant to all normal industrial cleaning solvents suitable for printed circuits.

Construction

The component is formed from a continuous band of E-beam welded precision resistive strip. Different resistance alloys are used based on the resistance value. The component is supplied without plating.

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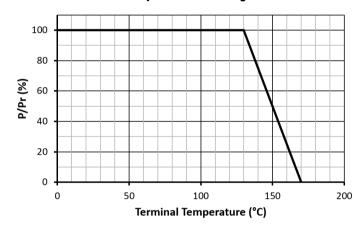




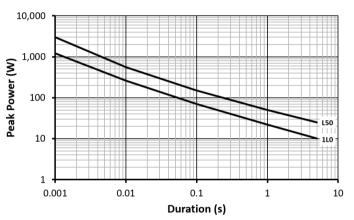
Performance Data

Test	Methods	Reference	ΔR
Load life	1000 hours, cyclic load at T _{Term} =130°C, P _{r130}	MIL-STD-202 Method 108	±1%
Short Term Overload	5 × P _{r130} for 5 s		±0.5%
High Temperature Exposure	2000 hours, T _A =170°C, unpowered	MIL-STD-202 Method 108	±1%
Low Temperature Storage	-65°C for 250hrs		±0.1%
Temperature Cycle	1000 cycles, -55°C to 150°C, 30 minutes dwell	JESD22 Method JA-104	±0.5%
Biased Humidity	1000 hours, 85°C/85%RH, 10% of P _{r130}	MIL-STD-202 Method 103	±0.5%
Vibration	10 - 2000Hz, 5g, 20min, 12 cycles/axis x 3 axes	MIL-STD-202 Method 204	±0.2%
Mechanical Shock	100g, 6ms, half-sine	MIL-STD-202 Method 213	±0.2%
Resistance to Solder Heat	260 ± 5°C, 10 ± 1s	MIL-STD-202 Method 210	±0.5%
Solderability	245 ± 5°C, 5 ± 0.5s	J-STD-002	>95% coverage
Resistance to Solvents	Clean with aqueous chemical	MIL-STD-202 Method 215	No damage

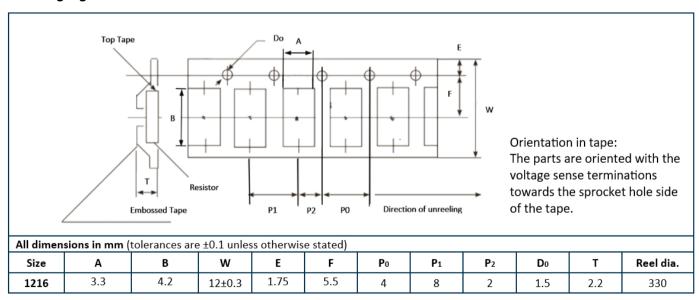
Temperature Derating



Single Pulse Performance



Packaging



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Ordering Procedure

Example: LRMAP1216A-1L0JT3 (1 milliohm ±5%, Pb-free)



1	2	3	4	5
Туре	Alloy	Value	Tolerance	Packing
LRMAP1216	Α		$D = \pm 0.5\%$	T3 = plastic tape, 3000/reel
	Е	3 characters	F = ±1%	
		L = milliohms	H = ±3%	
			J = ±5%	